mysh makefile:

CC = gcc

LDFLAGS = -lreadline

CFLAGS = -pedantic -g -std=c99

DEPS = strings.h shell.h

OBJ = mysh.o

EXEC = mysh

.PHONY: clean

%.o: %.c $(DEPS)

$(CC) -c -o $@ $< $(CFLAGS) $(LDFLAGS)

../$(EXEC): $(OBJ)

$(CC) -o $@ $^ $(CFLAGS) $(LDFLAGS)

clean:

rm -f \*.o

myls makefile:

CC = gcc

LDFLAGS = -lm

CFLAGS = -Wall -pedantic -g -std=c99

DEPS = myls.h

OBJ = myls.o print.o util.o

EXEC = myls

%.o: %.c $(DEPS)

$(CC) -c -o $@ $< $(CLFAGS) $(LDFLAGS)

../$(EXEC): $(OBJ)

$(CC) -o $@ $^ $(CFLAGS) $(LDFLAGS)

.PHONY: clean

clean:

rm -f \*.o

Tracing as the shell deconstructs a chain of pipes:

